

N3PT035MP330K

3300 V 35 mΩ Silicon Carbide MOSFET

Features

- State-of-the-art SiC MOSFET technology
- Reliable gate oxide process
- Ultra-low output capacitance
- Best-in-class figure-of-merits, [Ron*Coss] and [Ron*Crss]
- Stable switching characteristics up to 175 °C

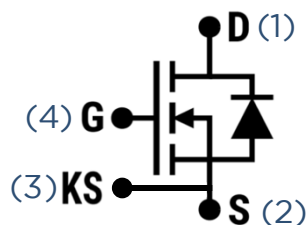
Benefits

- Higher system efficiency
- Reduced cooling requirements
- Increased power density
- Increased system switching frequency
- Enhanced system reliability
- Reduced total harmonic distortion

Maximum Ratings

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	Note
Drain-Source Voltage	$V_{(BR)DSS}$	$T_C = 25\text{ °C}$	3300	-	-	V	
Gate-Source Voltage	$V_{GS(max)}$		-10	-	25	V	
	$V_{GS,op}$	Recommended Operation	-	-5/+20	-		
Continuous Drain Current	I_D	$V_{GS} = 20\text{ V}, T_C = 25\text{ °C}$	-	-	88	A	Fig. 13
		$V_{GS} = 20\text{ V}, T_C = 100\text{ °C}$	-	-	63		
Pulsed Drain Current	$I_{D(pulse)}$	$T_C = 25\text{ °C}$	-	-	240	A	Fig. 12
Power Dissipation	P_{tot}	$T_C = 25\text{ °C}$	-	-	789	W	Fig. 14
Operating and Storage Temperature	T_J, T_{stg}		-55	-	175	°C	

V_{DS}	I_D	$R_{DS(on)}$	Package
3300 V	88 A	35 mΩ	TO-247-4



(1) (2) (3) (4)

Applications

- Utility-scale renewable/ESS/HVDC interfaces, modular multi-level converters, SSTs, DC-as-a-Service
- Defense/Maritime medium voltage converters & Directed Energy/Pulse Power
- Industrial medium voltage drives & VFDs
- Data-Center/AI Infrastructure (UPS, SST, rectifier, and protection -stages)
- Medium voltage medical imaging & therapy

Thermal and Package Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	Note
Thermal Resistance, Junction to Case	R_{thJC}		-	0.13	0.62	°C/W	Fig. 11
Thermal Resistance, Junction to Ambient	R_{thJA}		-	-	40	°C/W	
Weight	W_T		-	6.6	-	g	
Solder Temperature	T_L	JEDEC J-STD-020	-	-	225	°C	
Mounting Torque	T_M	M3 or 6-32 screw	-	0.9	-	Nm	

Electrical Characteristics ($T_c = 25\text{ °C}$ unless otherwise specified)

STATIC CHARACTERISTICS

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	Note
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}$, $I_D = 100\text{ QA}$	3300	-	-	V	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 0\text{ V}$, $V_{GS} = 0\text{ V}$	-	1	100	QA	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS} = V_{DS}$, $I_D = 10\text{ mA}$	1.8	2.6	3	V	
Gate-Source Leakage Current	I_{GSS}	$V_{GS} = -10 / +25\text{ V}$, $V_{DS} = 0\text{ V}$	-	-	±100	nA	
Transconductance	g_{fs}	$V_{DS} = 10\text{ V}$, $I_D = 40\text{ A}$	-	20.2	-	S	Fig. 8
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 20\text{ V}$, $I_D = 40\text{ A}$	-	33	40	mΩ	Fig. 1
		$V_{GS} = 20\text{ V}$, $I_D = 40\text{ A}$, $T_C = 175\text{ °C}$	-	101	-	mΩ	Fig. 3
		$V_{GS} = 18\text{ V}$, $I_D = 40\text{ A}$	-	35	-	mΩ	Fig. 1
		$V_{GS} = 18\text{ V}$, $I_D = 40\text{ A}$, $T_C = 175\text{ °C}$	-	102	-	mΩ	Fig. 3

DYNAMIC CHARACTERISTICS

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	Note
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = 1700\text{ V},$ $V_{AC} = 25\text{ mV}, f = 100\text{ kHz}$	-	8630	-	pF	Fig. 10
Output Capacitance	C_{oss}		-	120	-		
Reverse Capacitance	C_{rss}		-	13.8	-		
Gate-Source Charge	Q_{GS}	$V_{DS} = 1000\text{ V},$ $V_{GS} = -5 / +20\text{ V}, I_D = 40\text{ A}$	-	81	-	nC	Fig. 15
Gate-Drain Charge	Q_{GD}		-	76	-		
Total Gate Charge	Q_G		-	482	-		
Internal Gate Resistance	$R_{G(int)}$	$V_{AC} = 25\text{ mV}, f = 1\text{ MHz}$	-	2.4	-	Ω	
Turn-On Switching Energy	E_{ON}	$V_{DD} = 1700\text{ V}, I_D = 40\text{ A},$ $V_{GS} = -5 / +20\text{ V}, R_{G(ext)} = 5\text{ Ω},$ $L = 500\text{ QH}$	-	2999	-	QJ	Fig. 16 Fig. 17 Fig. 18
Turn-Off Switching Energy	E_{OFF}		-	712	-		
Total Switching Energy	E_{TOT}		-	3711	-		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 1700\text{ V}, I_D = 40\text{ A},$ $V_{GS} = -5 / +20\text{ V}, R_{G(ext)} = 5\text{ Ω},$ $L = 500\text{ QH}$ Timing relative to V_{DS} Inductive Load	-	31	-	ns	Fig. 19
Rise Time	t_r		-	48	-		
Turn-Off Delay Time	$t_{d(off)}$		-	111	-		
Fall Time	t_f		-	25	-		

BODY DIODE CHARACTERISTICS

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	Note
Diode Forward Voltage	V_{SD}	$V_{GS} = -5\text{ V}, I_{SD} = 40\text{ A}$	-	5.3	-	V	Fig. 20
		$V_{GS} = -5\text{ V}, I_{SD} = 40\text{ A}, T_J = 175\text{ °C}$	-	4.5	-	V	Fig. 21
Continuous Diode Forward Current	I_S	$V_{GS} = -5\text{ V}$	-	104	-	A	
Reverse Recovery Time	t_{rr}	$V_R = 1700\text{ V}, I_{SD} = 40\text{ A},$ $V_{GS} = -5\text{ V}, diF/dt = 2500\text{ A/Qs}$	-	22	-	ns	
Reverse Recovery Charge	Q_{rr}		-	544	-	nC	
Peak Reverse Recovery Current	I_{RRM}		-	38	-	A	

Typical Performance

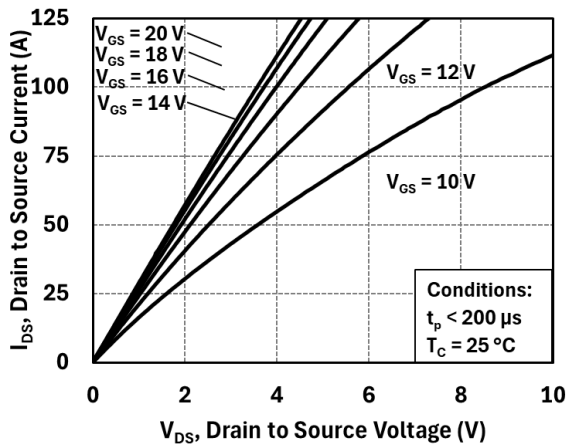


Figure 1: Output Characteristics at 25 °C

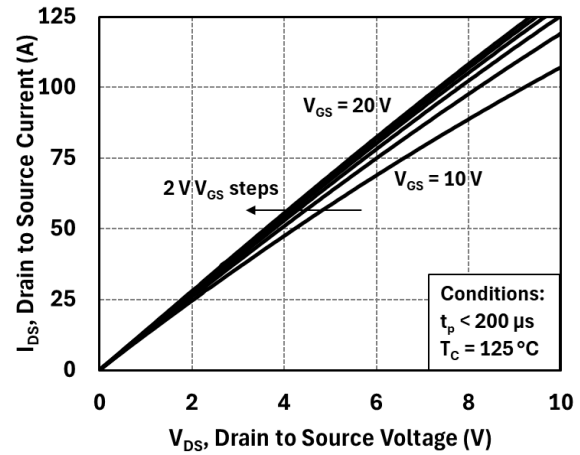


Figure 2: Output Characteristics at 125 °C

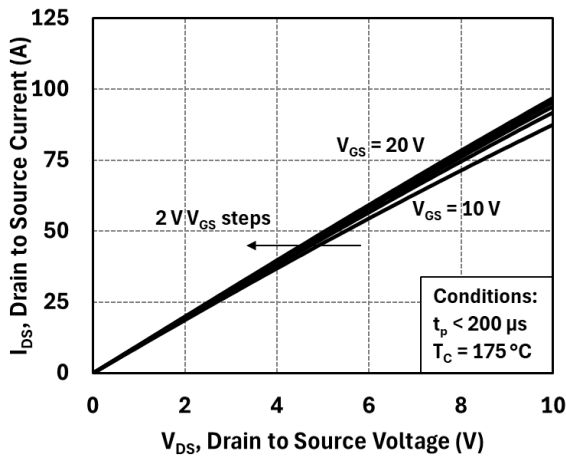


Figure 3: Output Characteristics at 175 °C

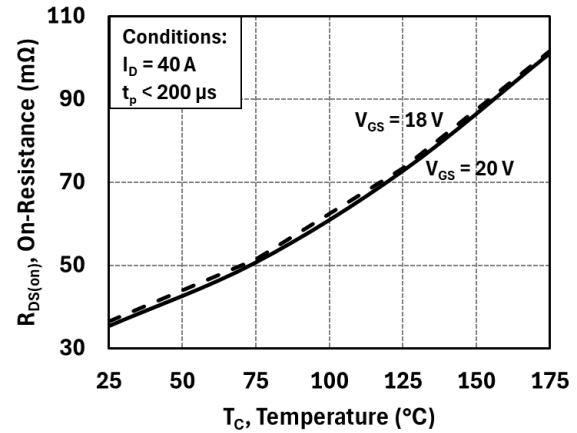


Figure 4: On-Resistance vs. Temperature

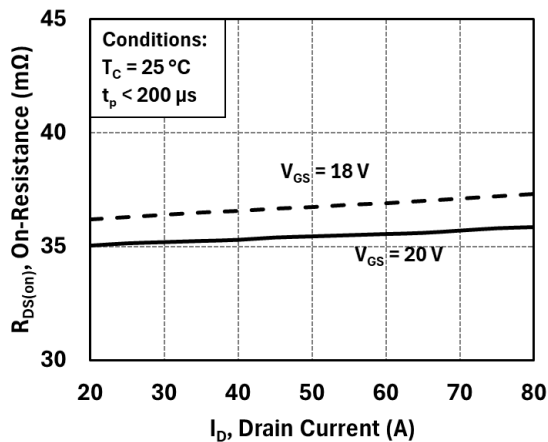


Figure 5: On-Resistance vs. Drain Current

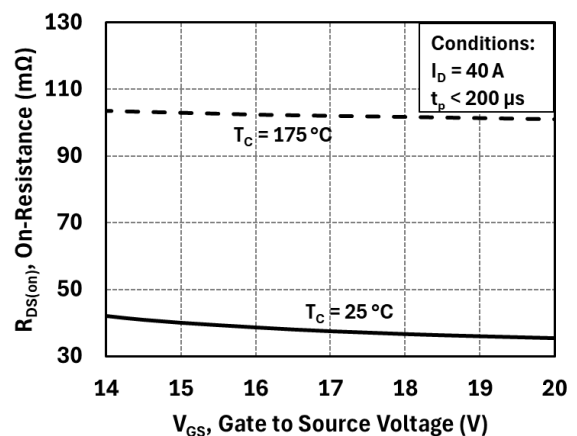


Figure 6: On-Resistance vs. Gate Voltage

Typical Performance

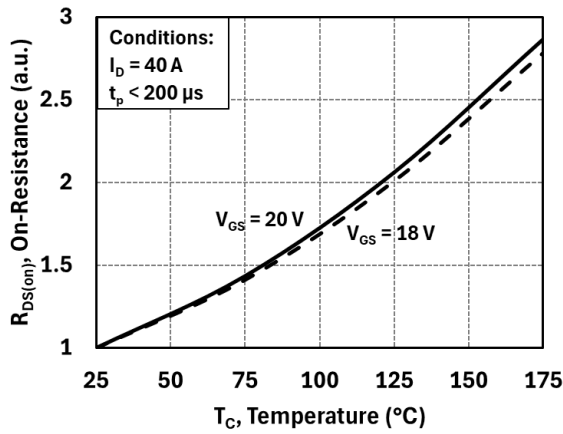


Figure 7: Normalized On-Resistance vs. Temperature

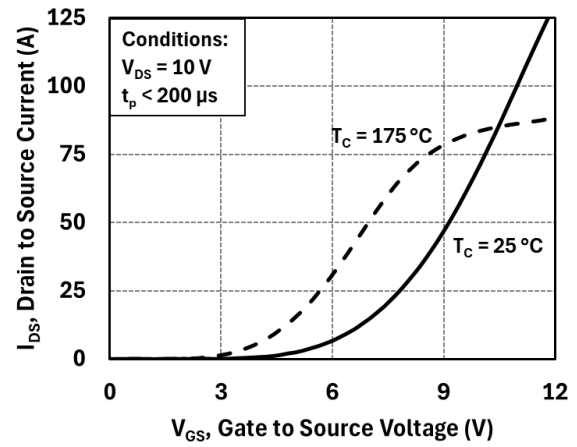


Figure 8: Transfer Characteristics

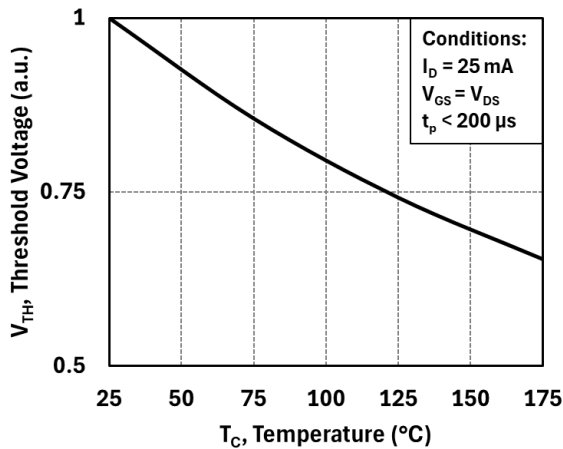


Figure 9: Threshold Voltage vs. Temperature

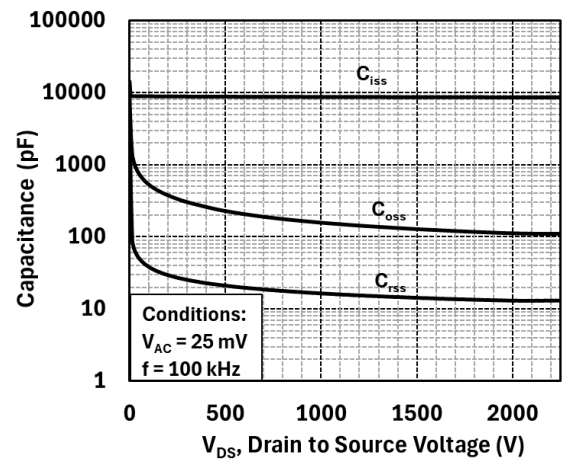


Figure 10: Capacitances vs. Drain-Source Voltage (0-1000 V)

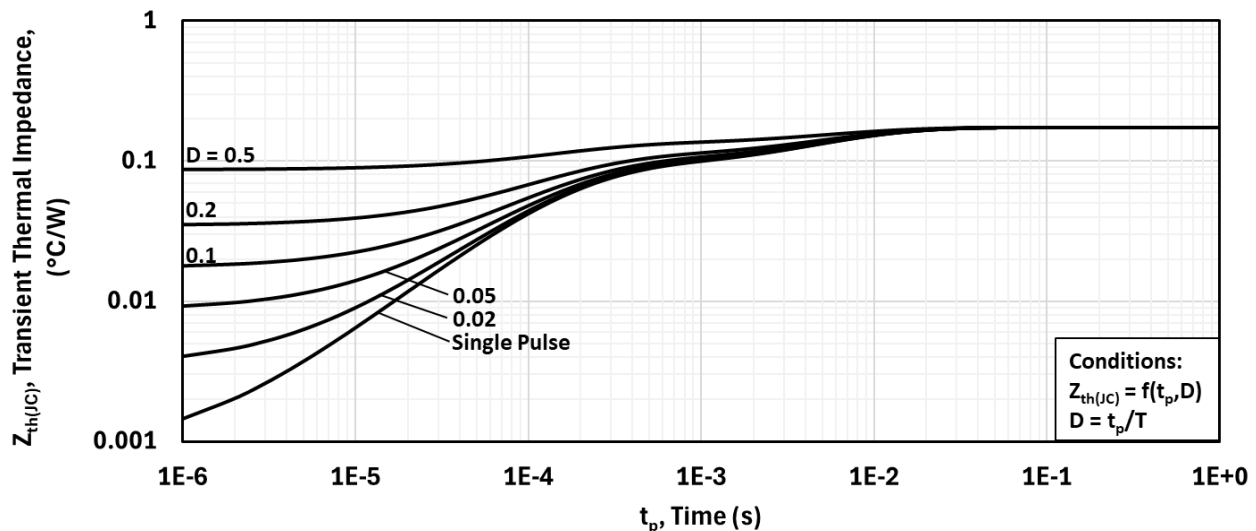


Figure 11: Transient Thermal Impedance

Typical Performance

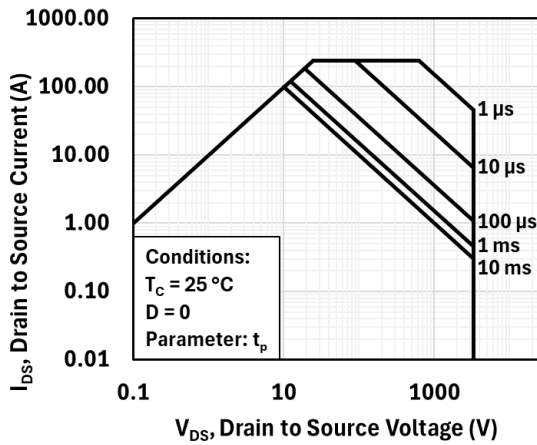


Figure 12: Safe Operating Area

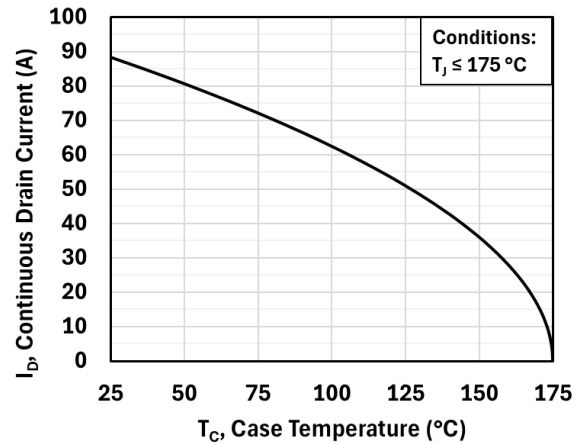


Figure 13: Current De-rating Curve

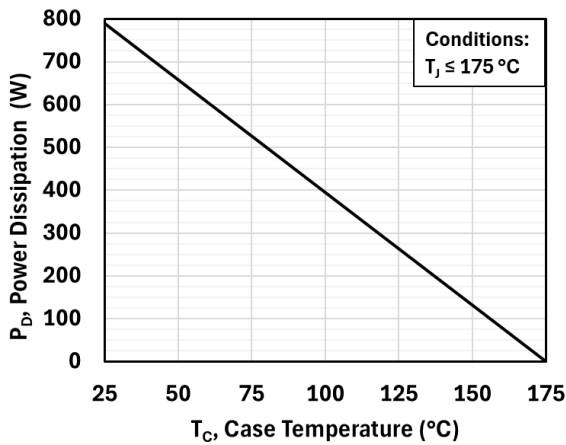


Figure 14: Power De-rating Curve

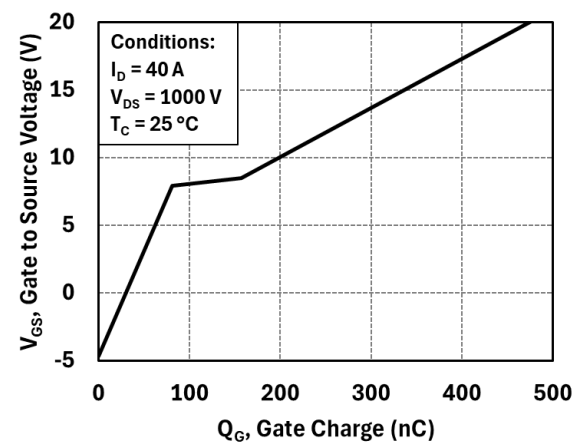


Figure 15: Gate Charge Characteristics

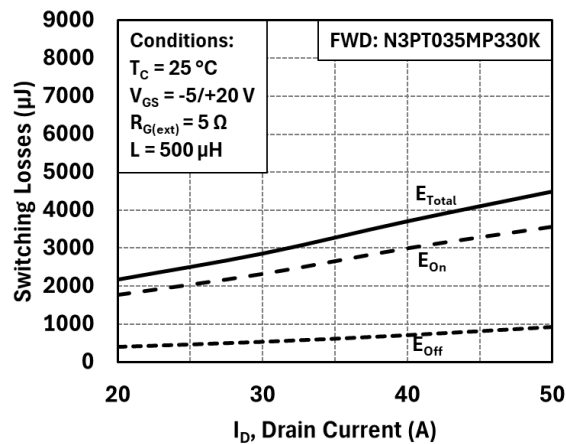


Figure 16: Inductive Switching Energy vs. Drain Current ($V_{DD} = 1700\text{ V}$)

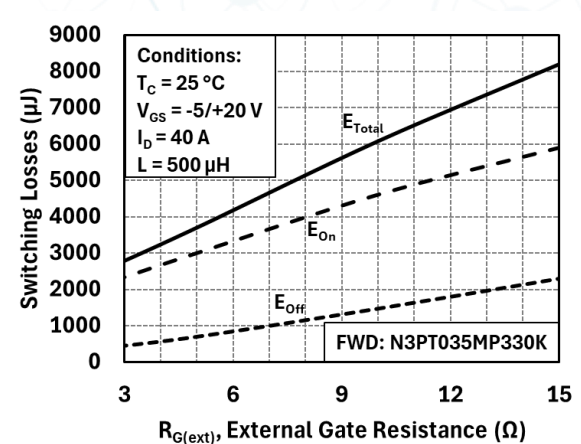


Figure 17: Inductive Switching Energy vs. $R_{G(ext)}$ ($V_{DD} = 1700\text{ V}$)

Typical Performance

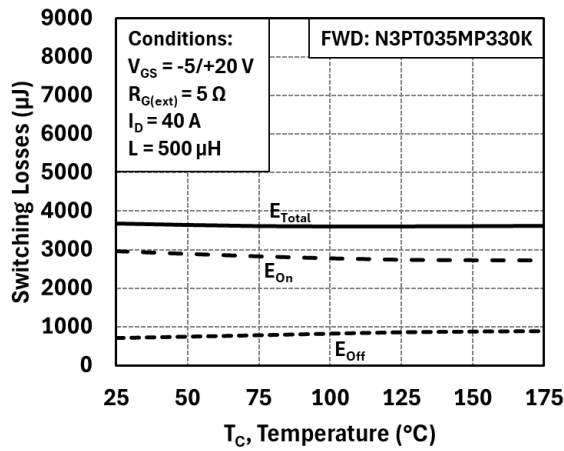


Figure 18: Inductive Switching Energy vs. Temperature ($V_{DD} = 1700 \text{ V}$)

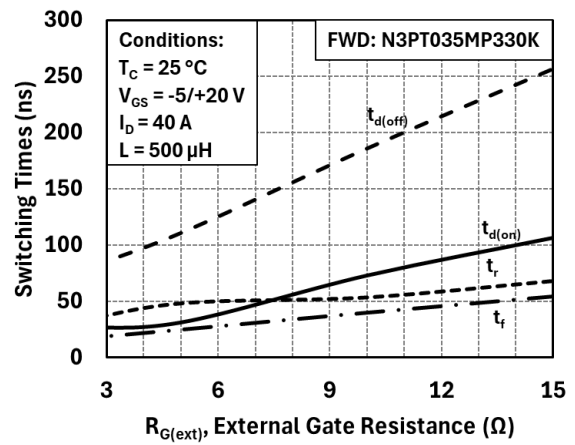


Figure 19: Switching Times vs. $R_{G(ext)}$ ($V_{DD} = 1700 \text{ V}$)

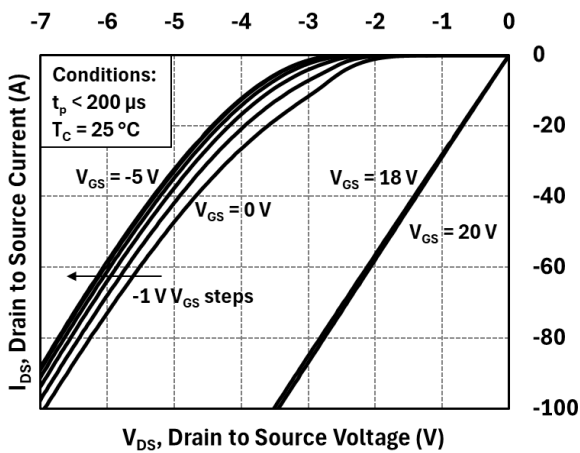


Figure 20: Body Diode Characteristics at 25°C

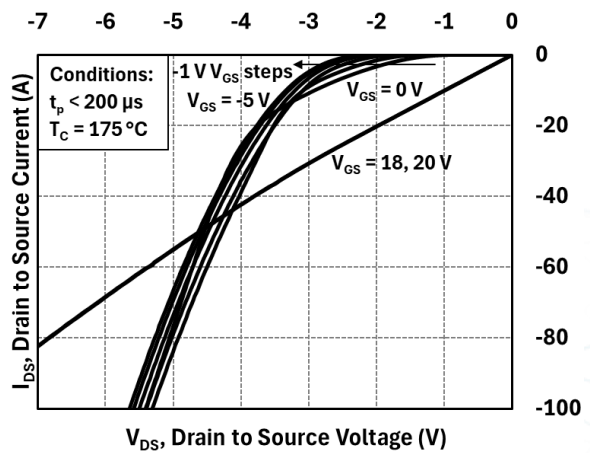


Figure 21: Body Diode Characteristics at 175°C

Dynamic Testing Circuit Schematics

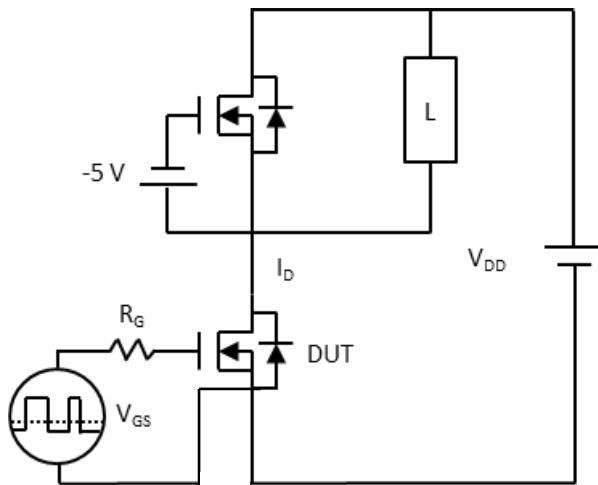


Figure 22: Inductive Load Switching Test Circuit

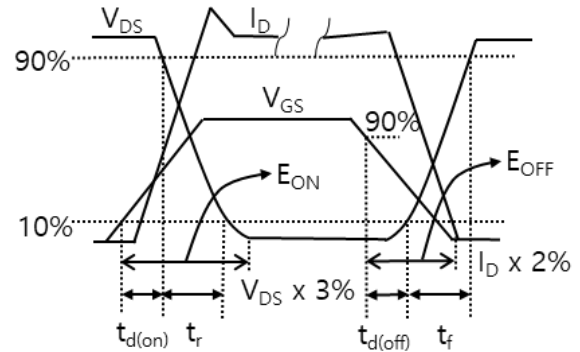


Figure 23: Inductive Load Switching Test Waveforms

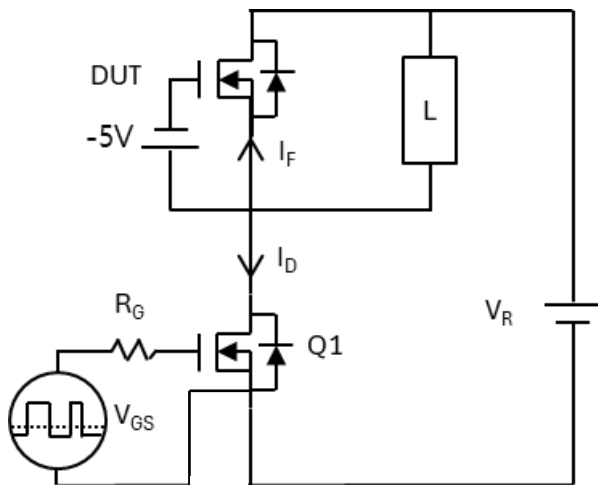


Figure 24: Reverse Recovery Test Circuit

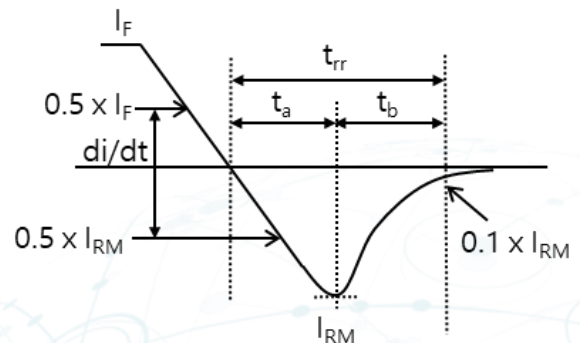


Figure 25: Body Diode Reverse Recovery Test Waveforms

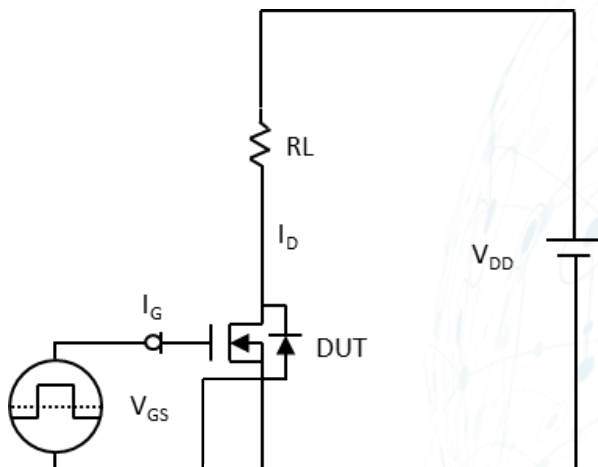


Figure 26: Gate Charge Test Circuit

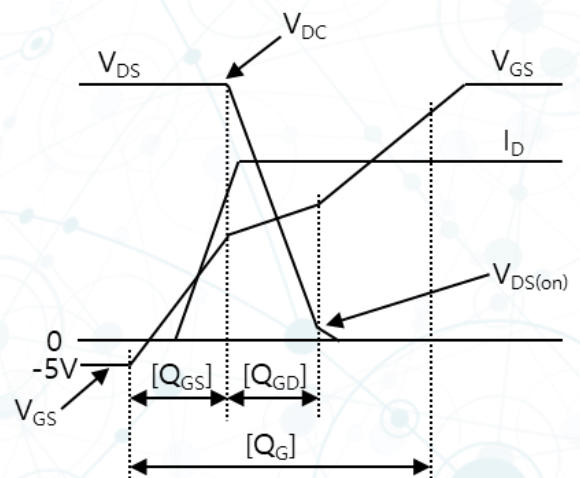
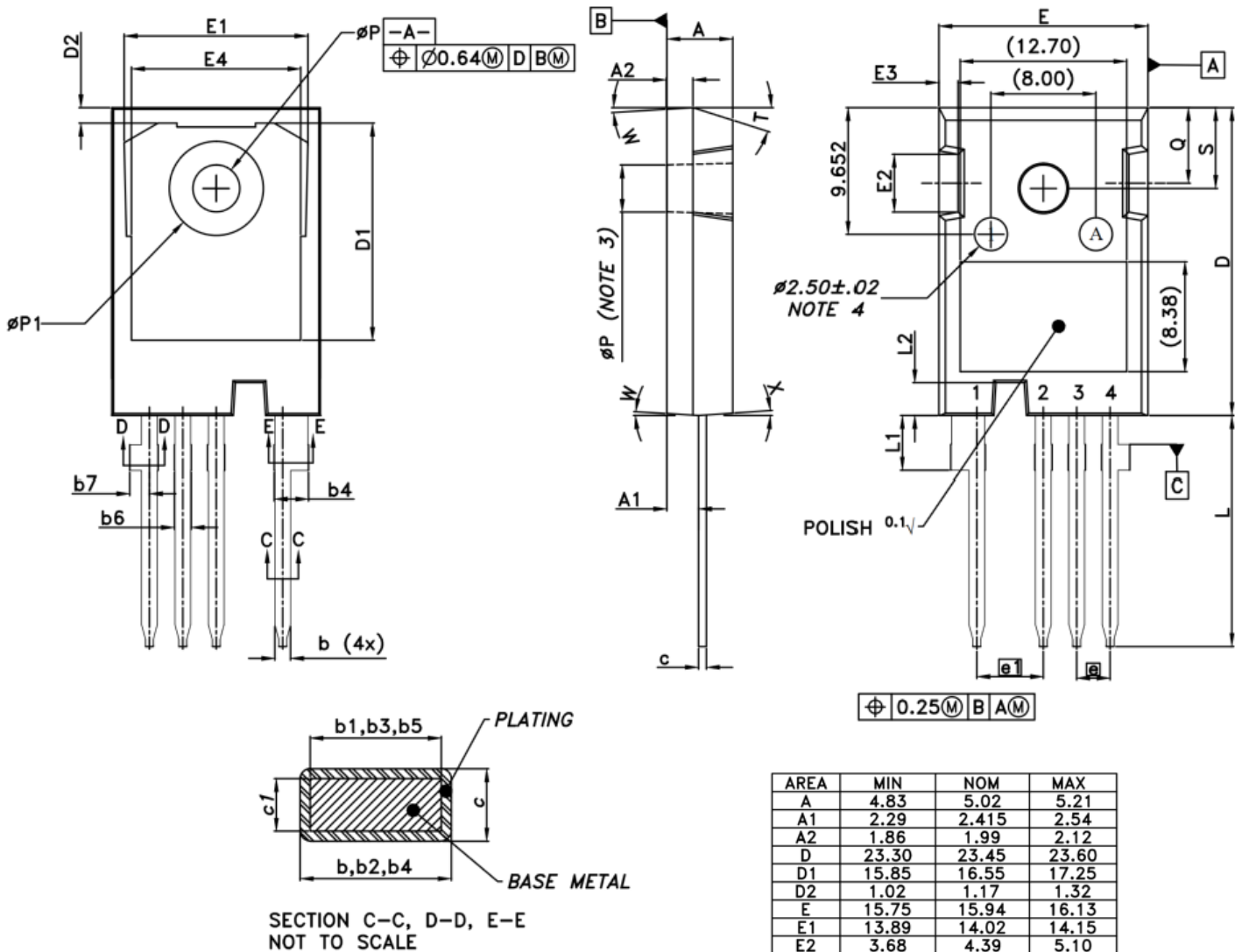


Figure 27: Gate Charge Test Waveforms



Package Dimensions



Warranty & Disclaimers

Since NoMIS Power does not control the use of its products, there are no express warranties that extend beyond the description herein.

THIS PRODUCT IS PROVIDED "AS IS". NOMIS POWER MAKES NO REPRESENTATIONS OR WARRANTIES OF ANY KIND WHETHER EXPRESS OR IMPLIED, WRITTEN OR ORAL, STATUTORY OR OTHERWISE, RELATED TO THE PRODUCT INCLUDING BUT NOT LIMITED TO ANY IMPLIED WARRANTIES OF NON-INFRINGEMENT, MERCHANTABILITY, AND FITNESS FOR A PARTICULAR PURPOSE, OR WARRANTIES RELATED TO ITS CONDITION, QUALITY, OR PERFORMANCE. IN NO EVENT WILL NOMIS POWER BE LIABLE FOR ANY INDIRECT, SPECIAL, PUNITIVE, INCIDENTAL, OR CONSEQUENTIAL LOSS, DAMAGE, COST, OR EXPENSE OF ANY KIND WHATSOEVER (INCLUDING, WITHOUT LIMITATION, DAMAGES FOR LOSS OF PROFIT, BUSINESS INTERRUPTION, OR LOSS OF INFORMATION) RELATED TO THE USE OF, OR INABILITY TO USE, THE PRODUCT, HOWEVER CAUSED, EVEN IF NOMIS POWER HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES OR THE DAMAGES ARE FORESEEABLE. TO THE FULLEST EXTENT ALLOWED BY LAW, NOMIS POWER'S TOTAL LIABILITY ON ALL CLAIMS IN ANY WAY RELATED TO THE PRODUCT OR ITS USE, OR INABILITY TO USE, WILL NOT EXCEED THE AMOUNT OF FEES, IF ANY, THAT YOU HAVE PAID DIRECTLY TO THE SELLER FOR THE PRODUCT.

NoMIS Power's products are not recommended or authorized for usage in equipment or applications that require extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury, such as life support or medical instruments or applications. Buyers assume all risk and liability for using our products in such applications and agree to defend, indemnify and hold harmless NoMIS Power from any and all damages, claims, suits or expenses resulting from such use.

NoMIS Power further does not warrant the accuracy or completeness of the information, text, graphics, links or other items contained in this Data Sheet. This Data Sheet and the information herein may be used only with NoMIS Power products, including to design, test, and integrate NoMIS Power products with your application. Use of this information in any other manner violates these terms. Information regarding device application is provided only for your convenience and may be superseded by updates at any time. It is your responsibility to ensure that your application meets with your specifications.

NoMIS Power may make changes to this Data Sheet at any time, or to the product described herein, at any time without notice.

No licenses are conveyed, implicitly or otherwise, under any NoMIS Power intellectual property rights.

NoMIS Power and  **NoMIS Power** are trademarks of NoMIS Power Corporation in the United States and/or other countries. NoMIS Power reserves the right to make changes without further notice to any products herein. NoMIS Power makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does NoMIS Power assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. NoMIS Power does not convey any license under its patent rights nor the rights of others.

NoMIS Power Corporation

251 Fuller Rd, Albany, NY 12203, USA

together@NoMISPower.com | NoMISPower.com